

FIG. 1

Prior Art

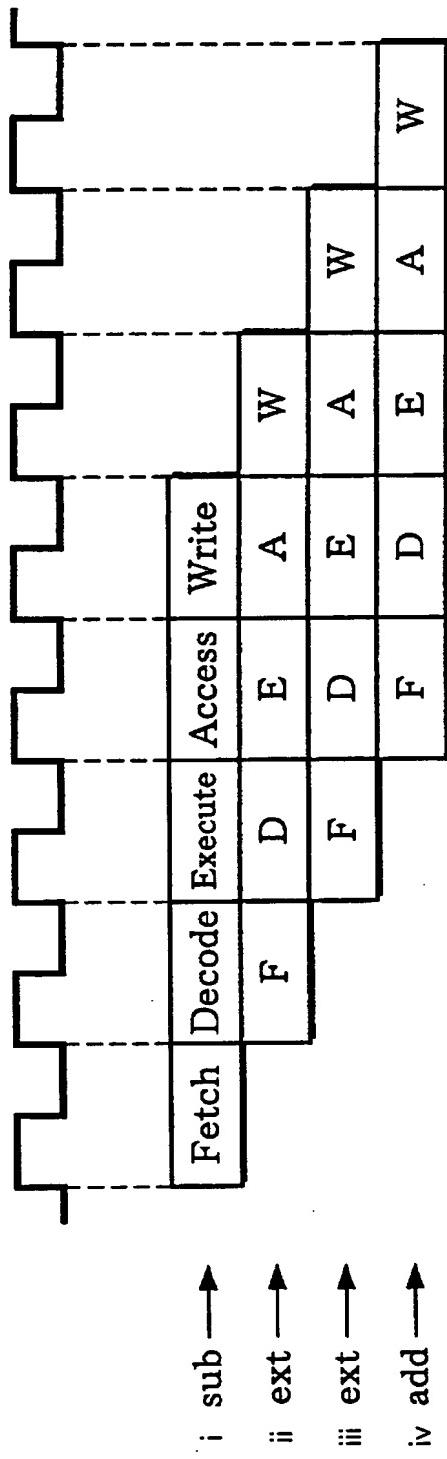


FIG. 2A 20 FETCH

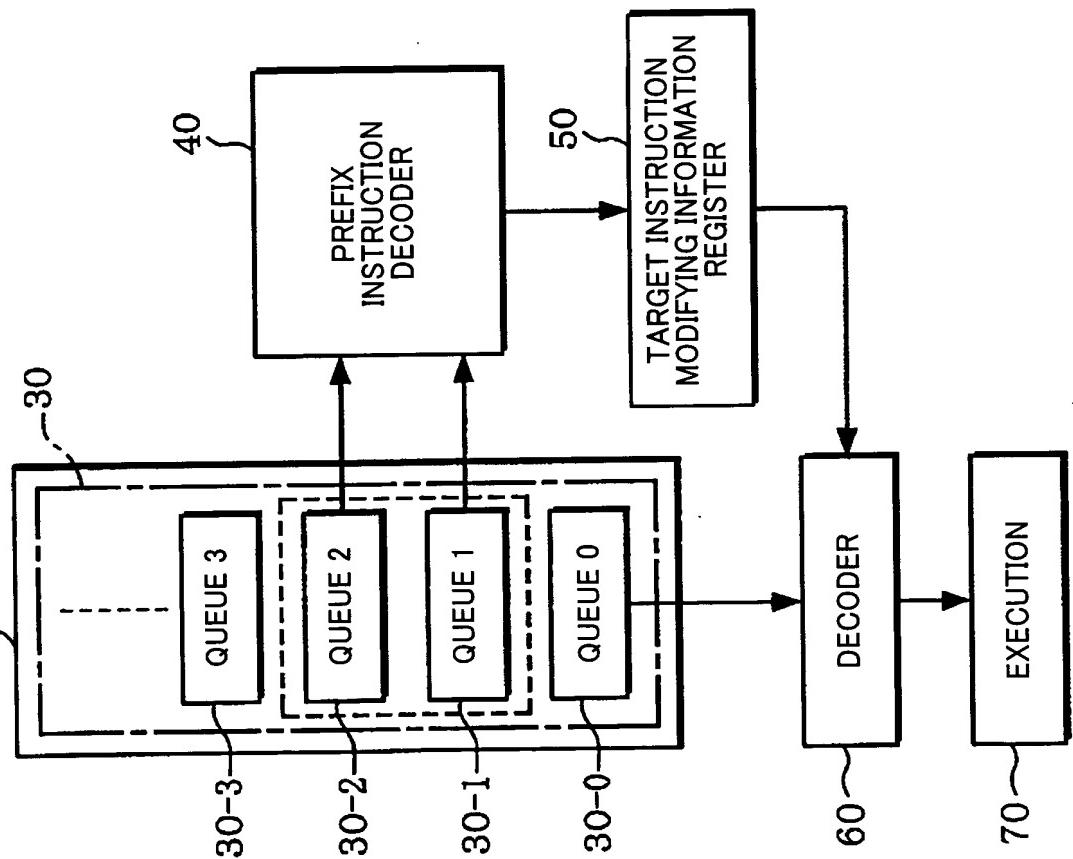


FIG. 2B 20

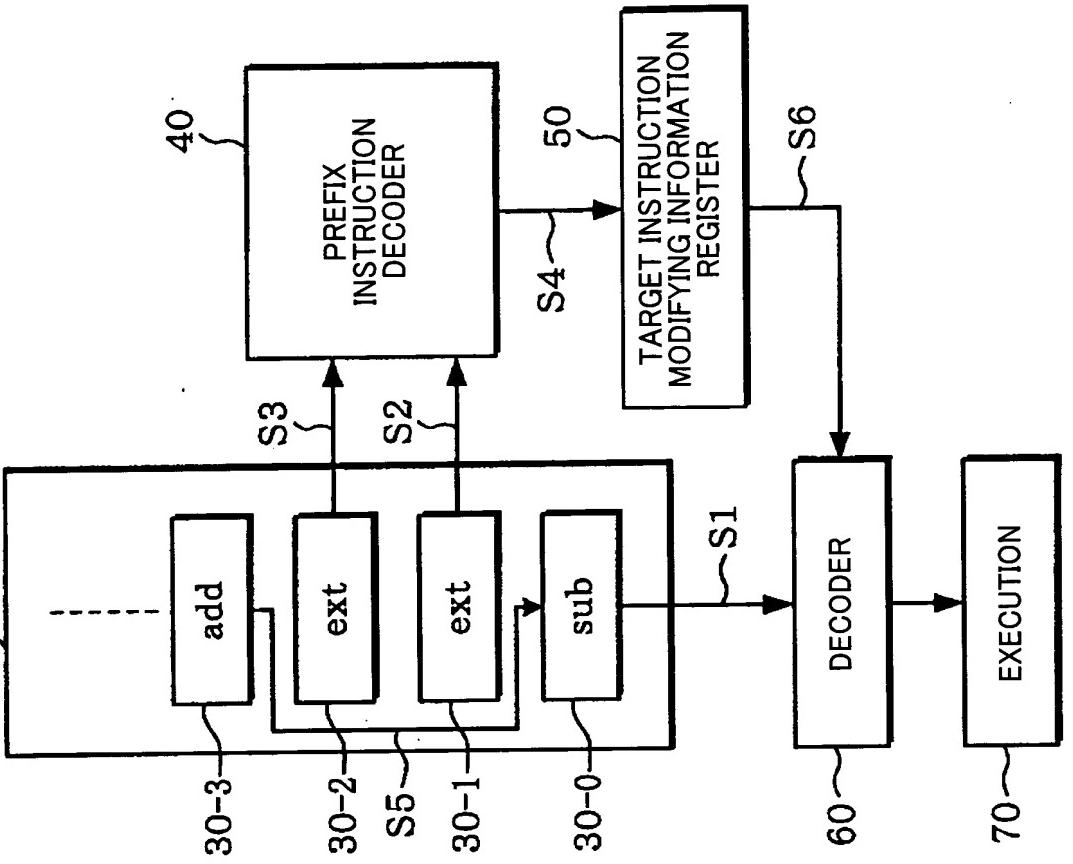
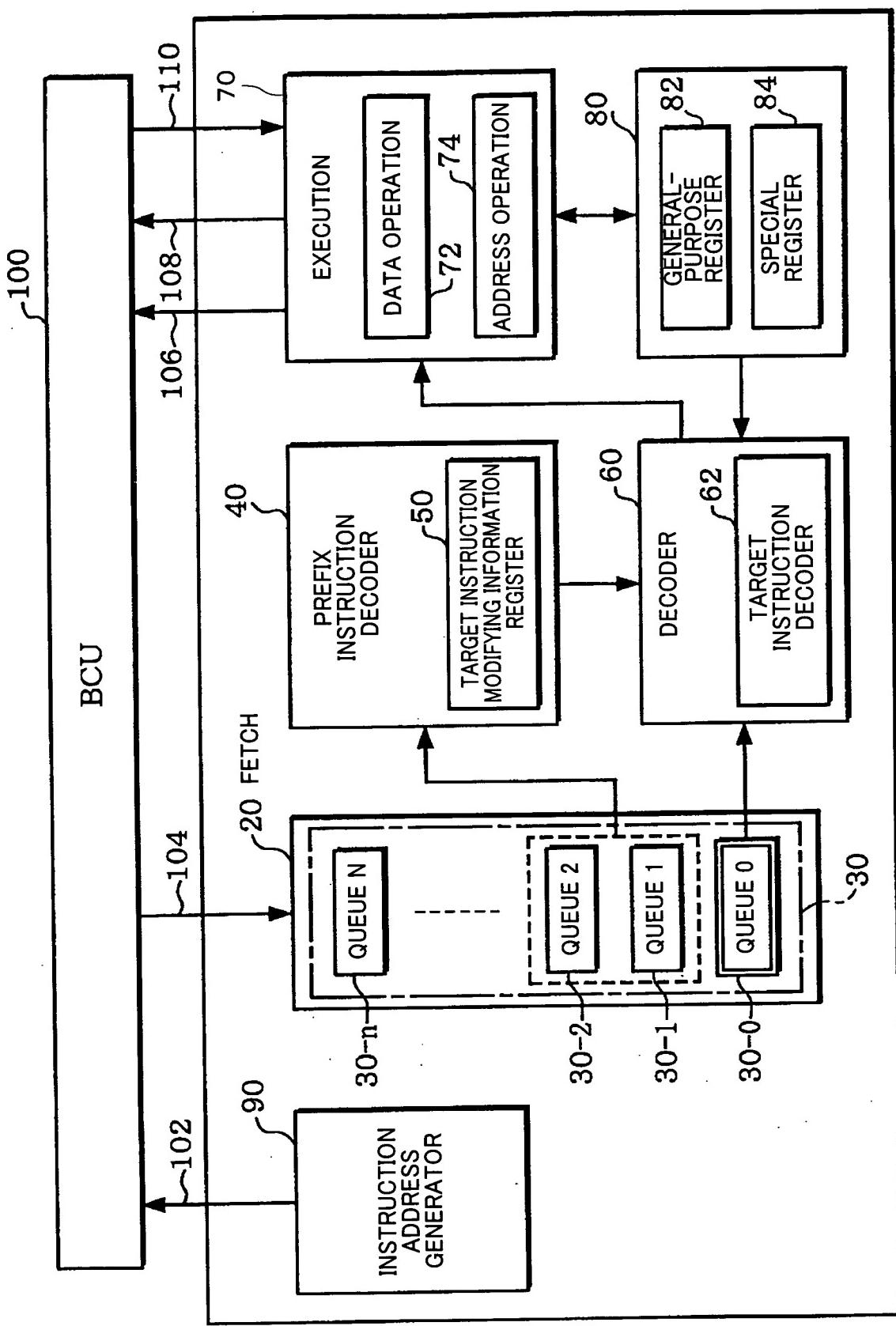


FIG. 3



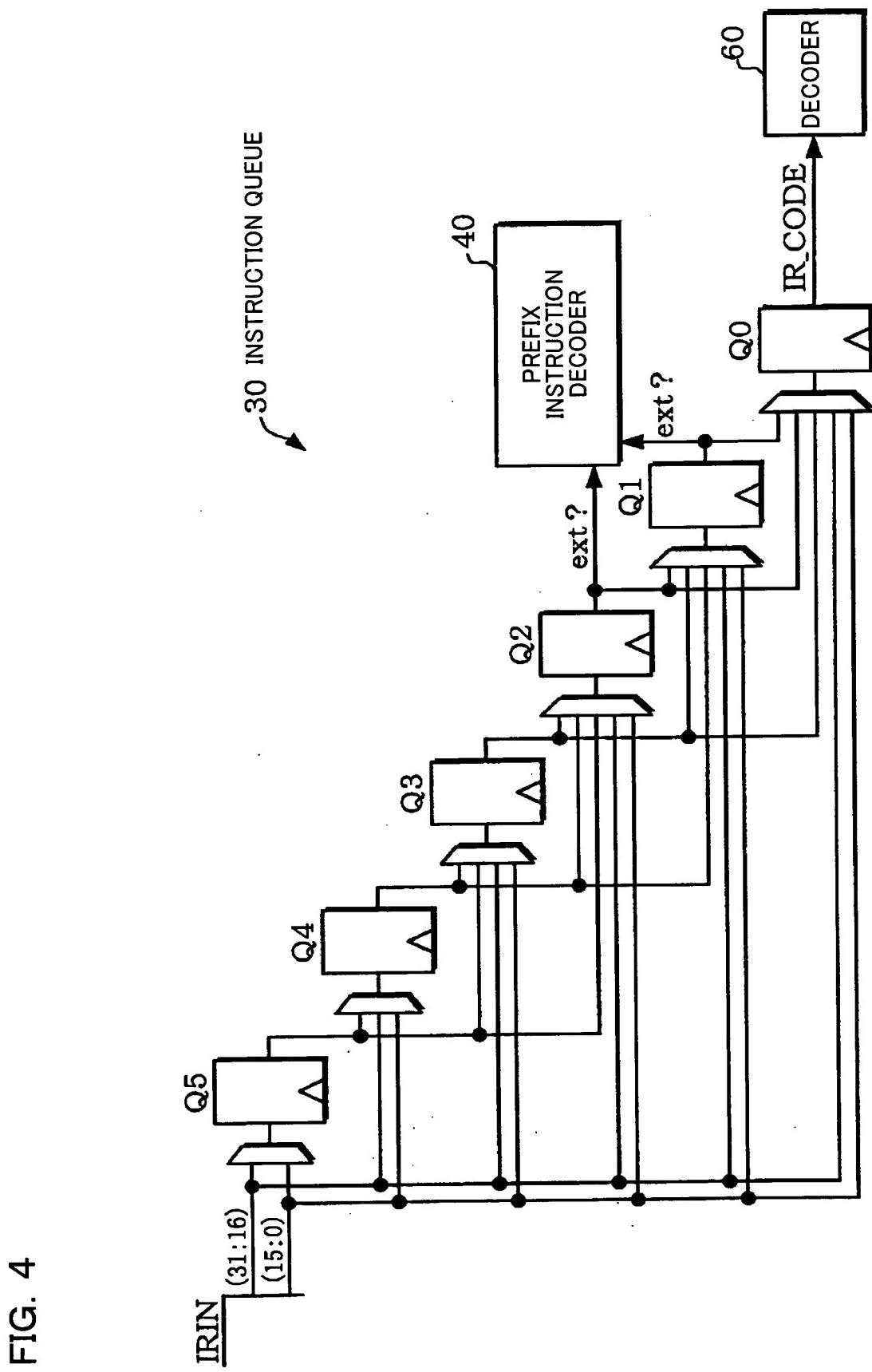


FIG. 5A

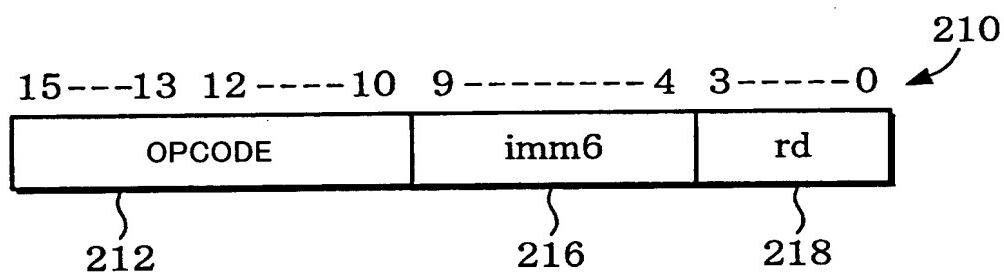


FIG. 5B

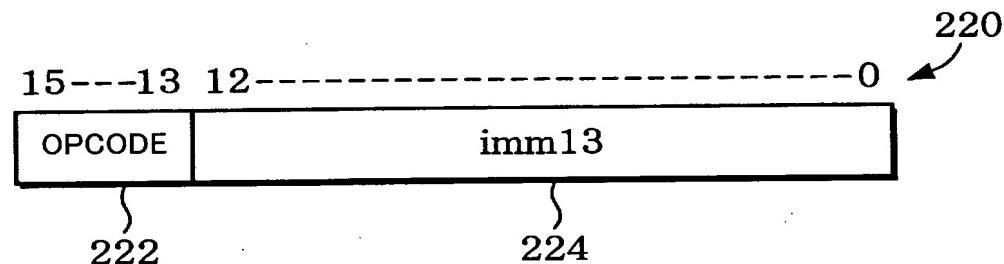


FIG. 6A

$rd = rd \ op. \ imm6$

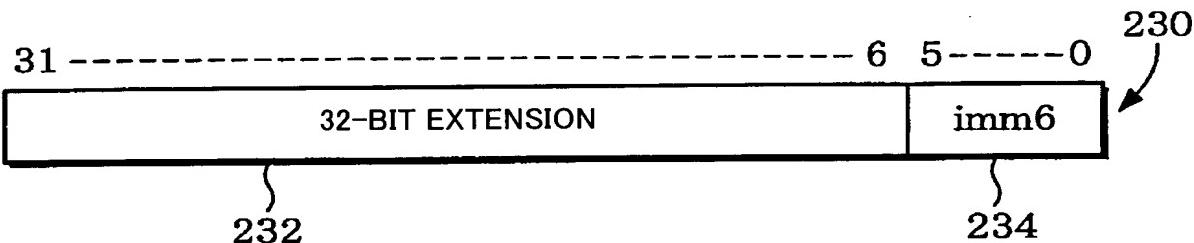


FIG. 6B

$rd = rd \ op. \ imm19$

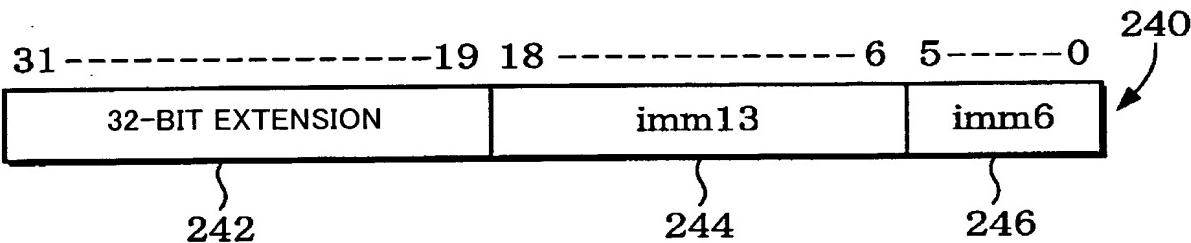


FIG. 6C

$rd = rd \ op. \ imm32$

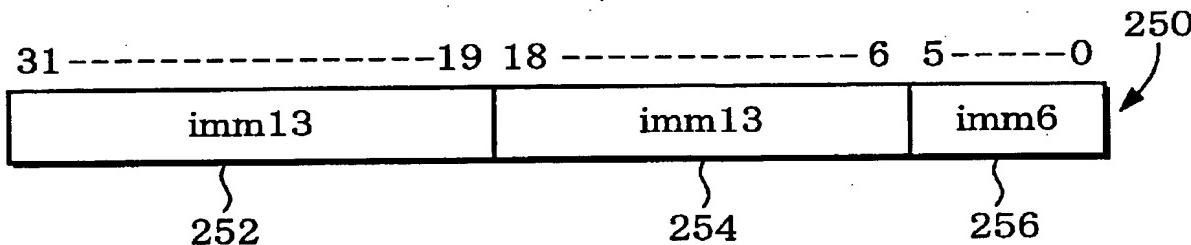


FIG. 7

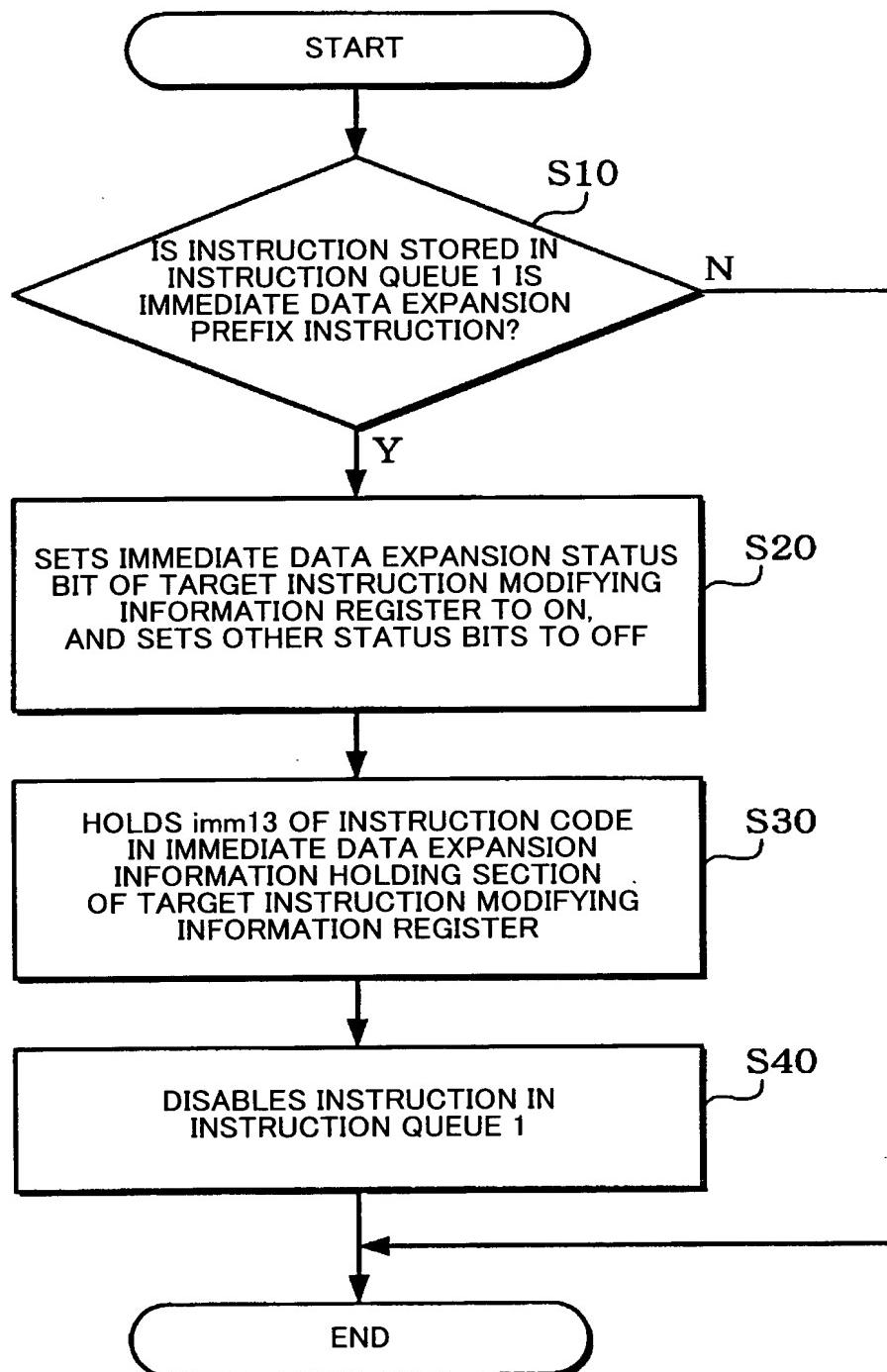


FIG. 8

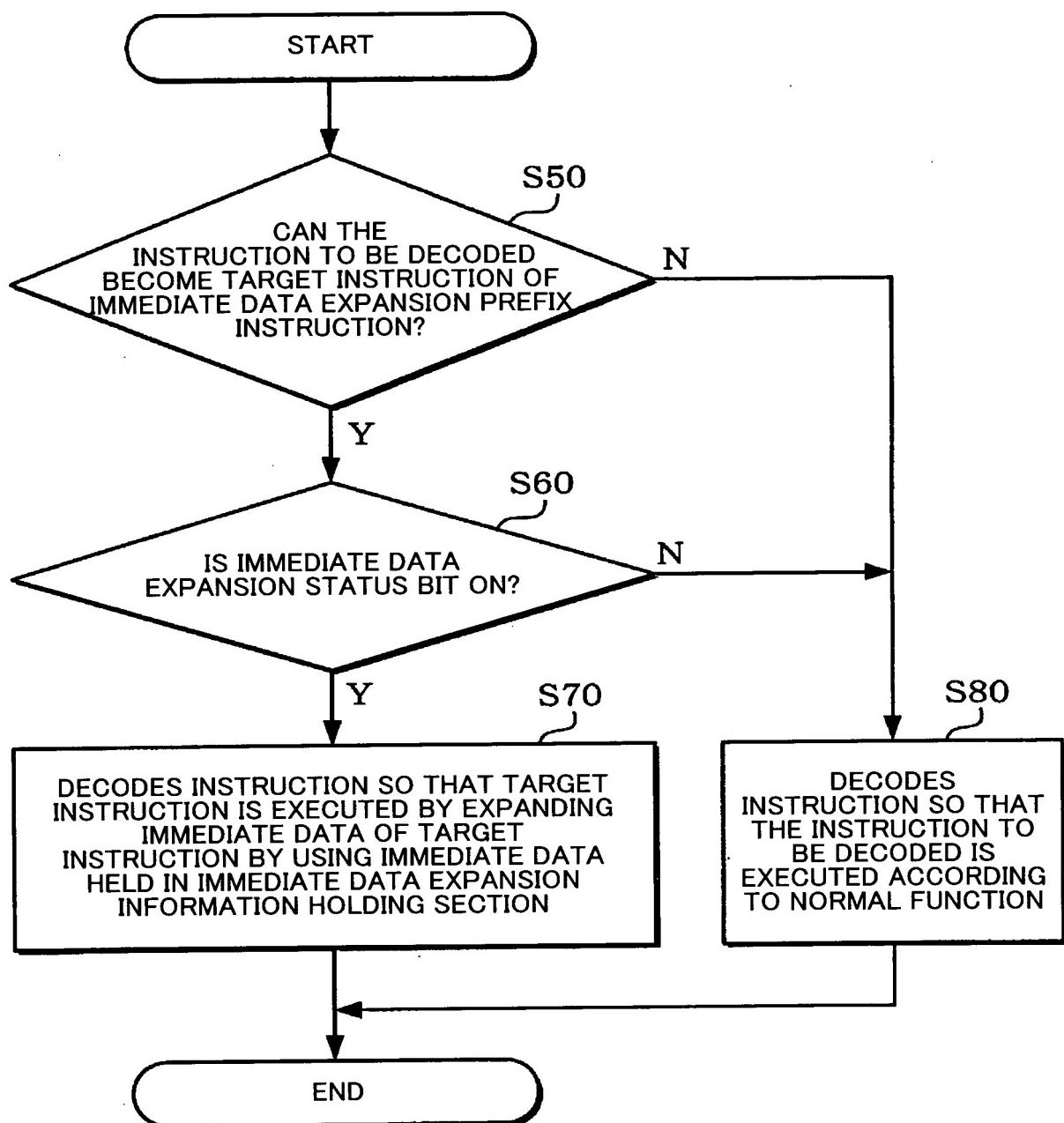


FIG. 9A

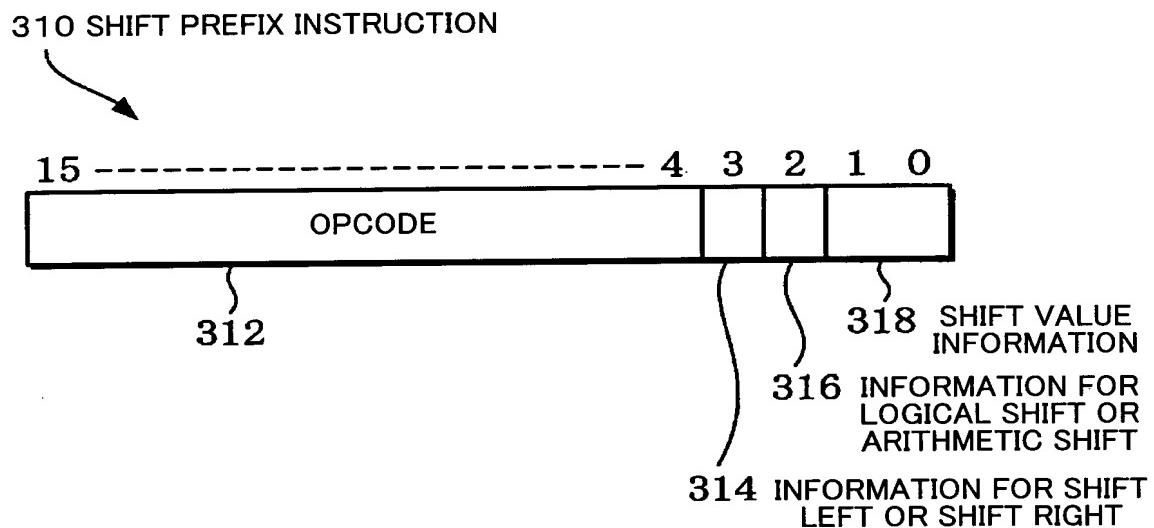


FIG. 9B

add %r2, 3 → r2 + 3

FIG. 9C

ext srl, 1 }
add %r2, 3 } → r2 = (r2+3) ≫ 1

FIG. 10

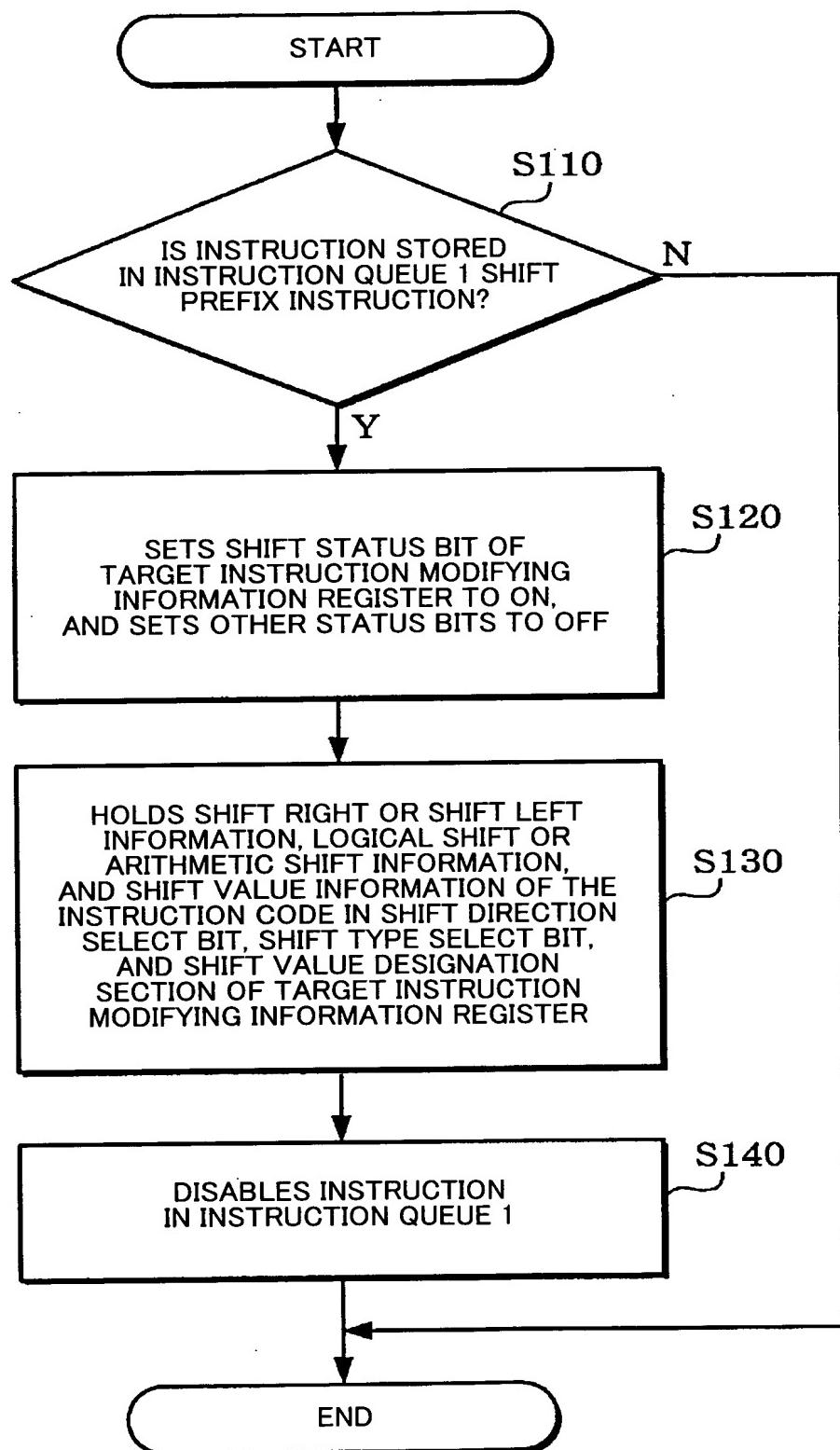


FIG. 11

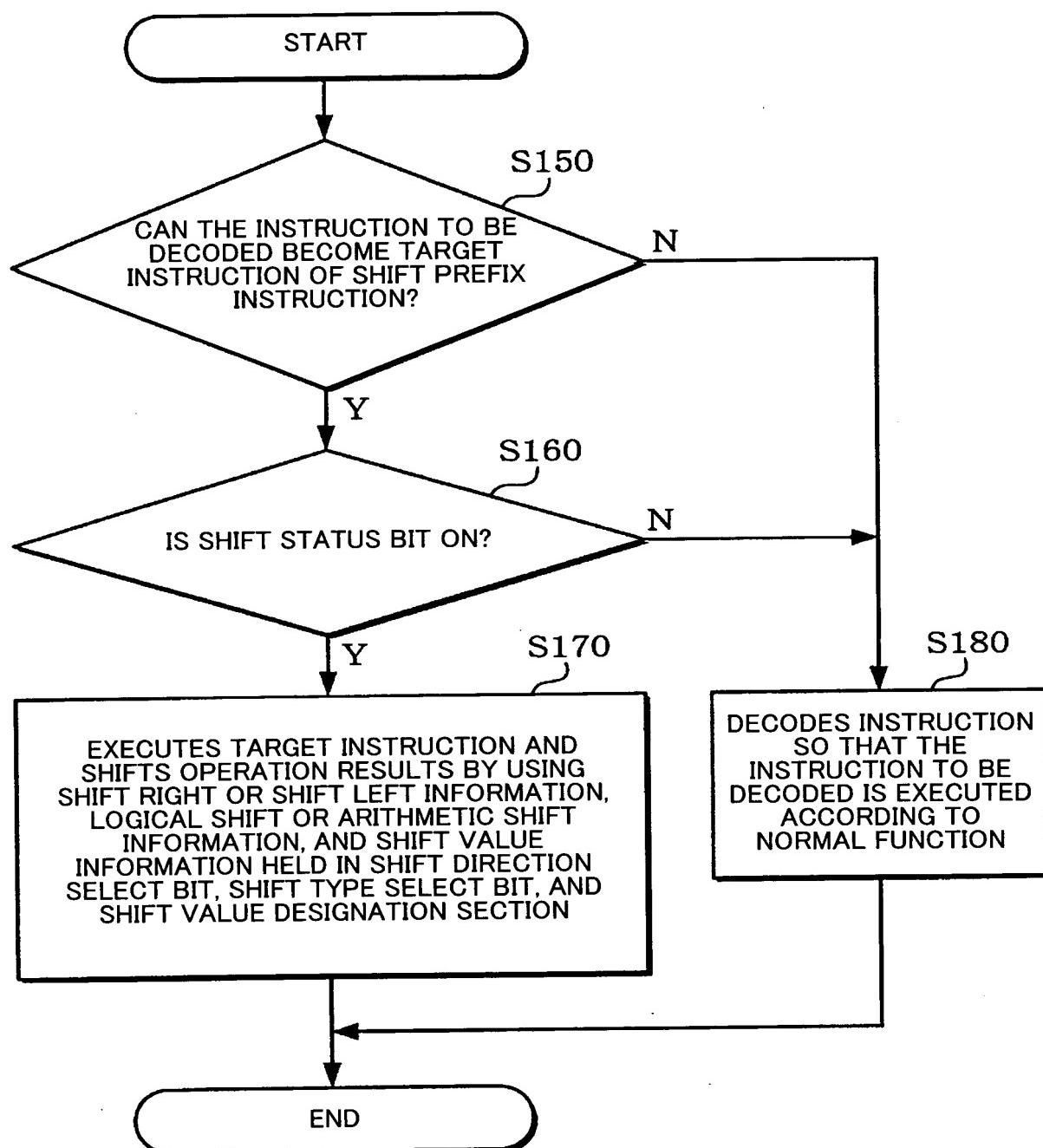


FIG. 12A

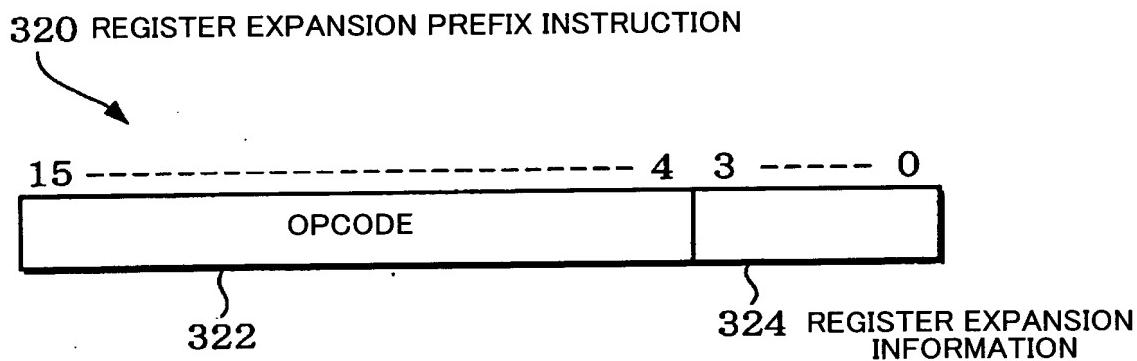


FIG. 12B

add %r2,3 → r2 = r2 + 3

FIG. 12C

ext %r1
add %r2,3 } → r1 = r2 + 3

FIG. 13

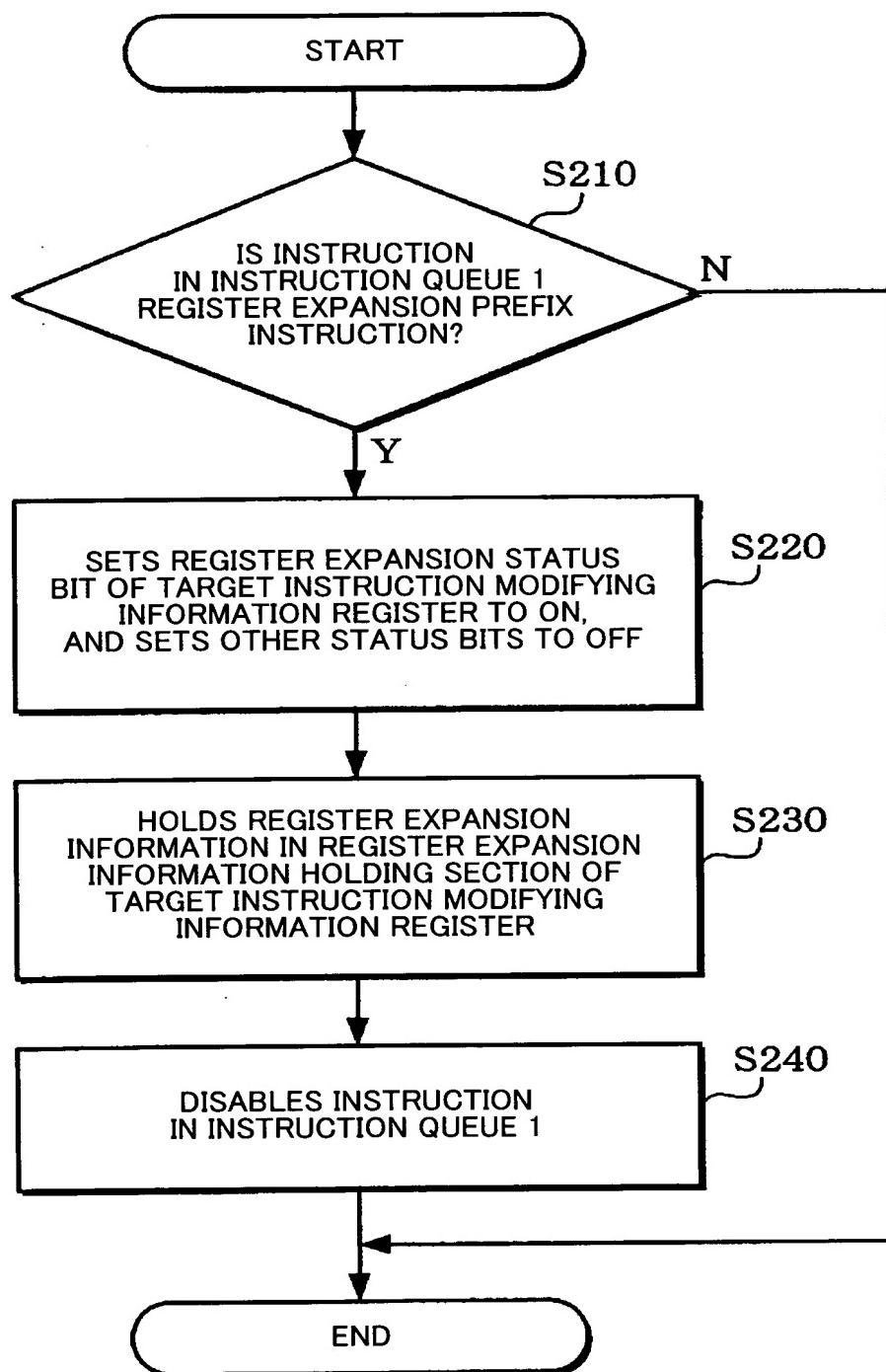


FIG. 14

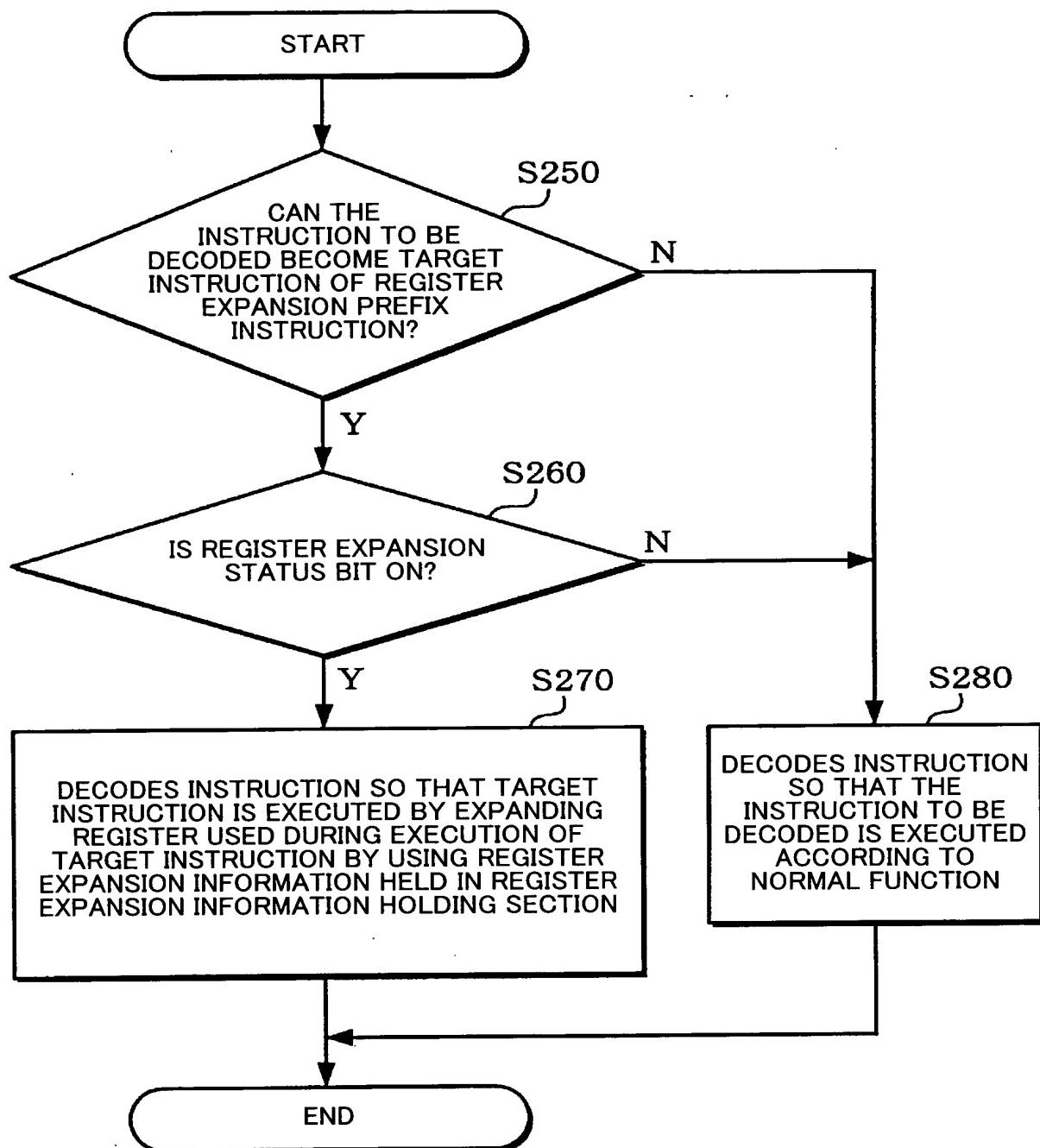


FIG. 15A

330 EXECUTION CONTROL PREFIX INSTRUCTION

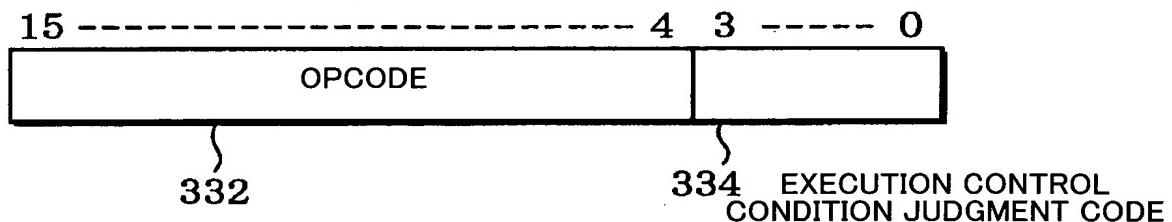


FIG. 15B

```
cmp    %r1    %r2
ext    EQ
add    %r1    1
```

FIG. 15C

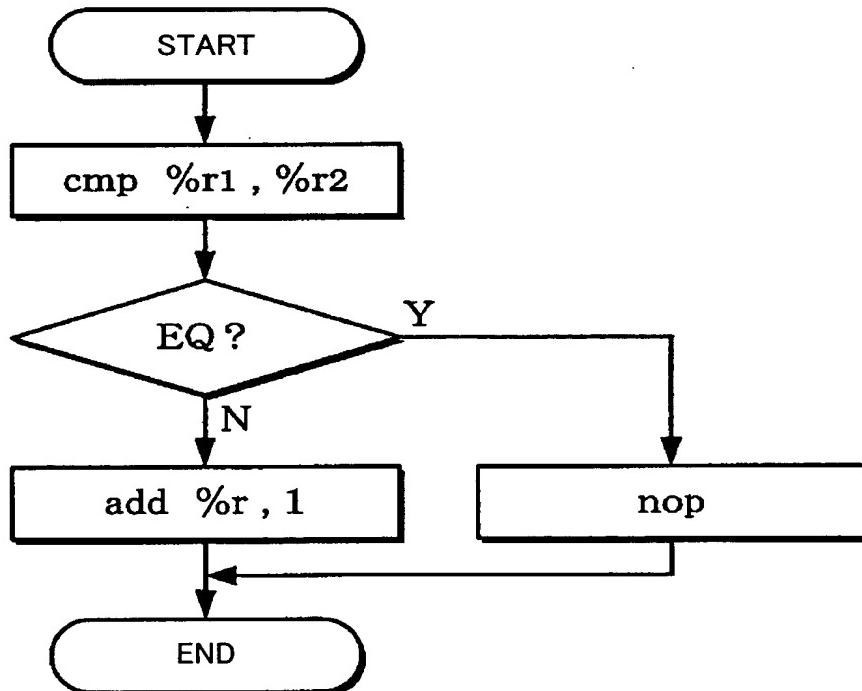


FIG. 16

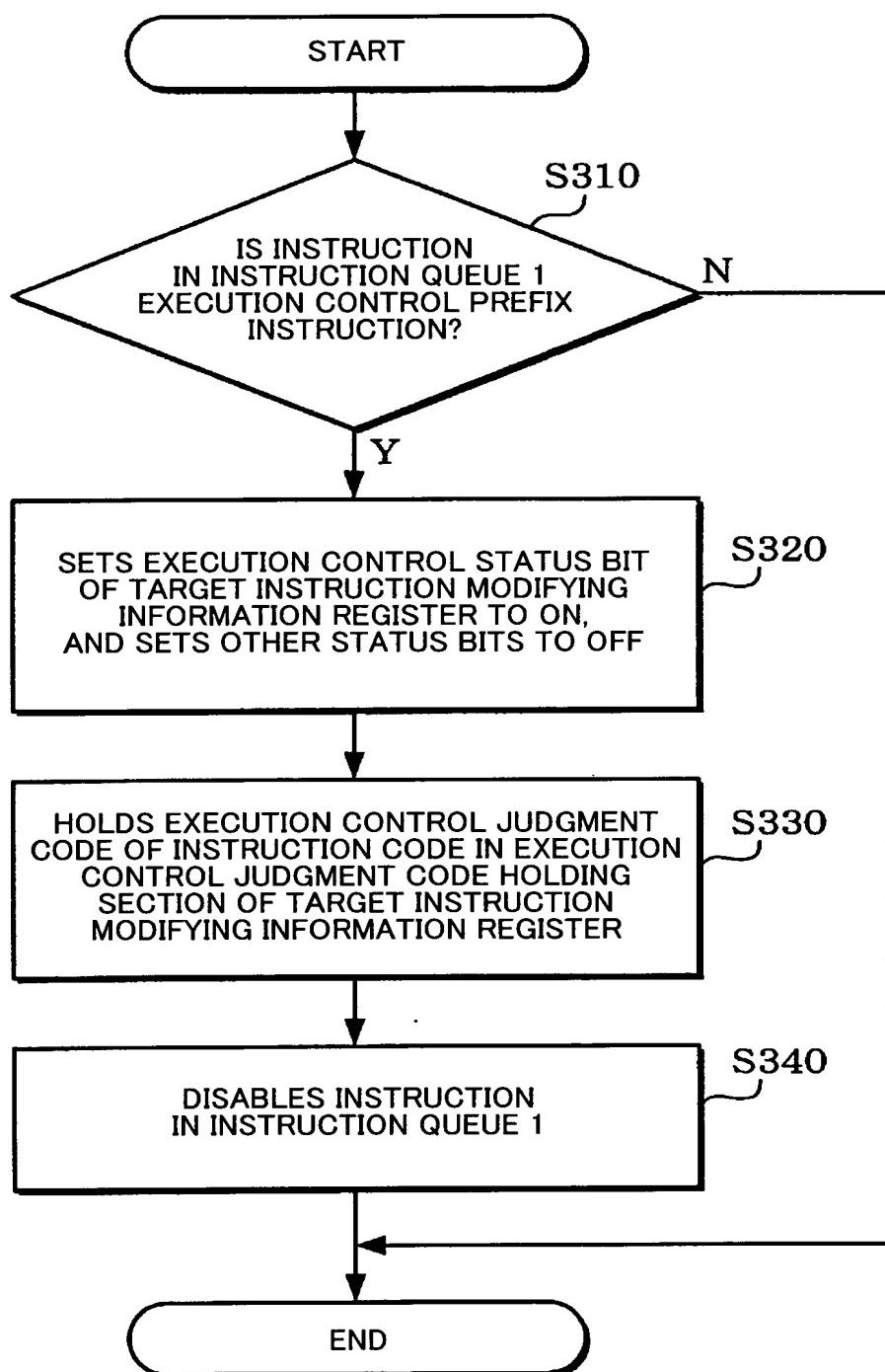


FIG. 17

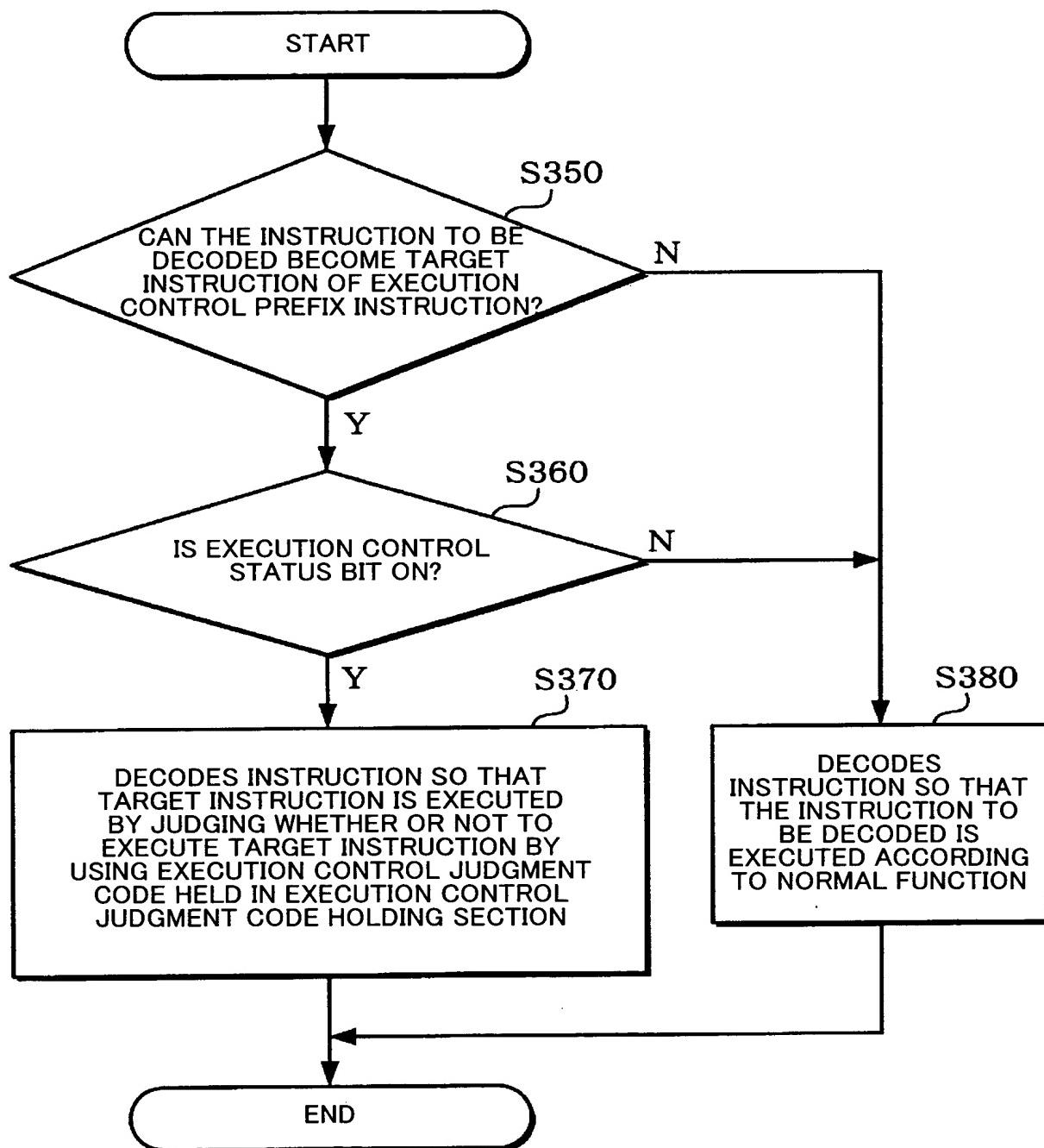


FIG. 18A

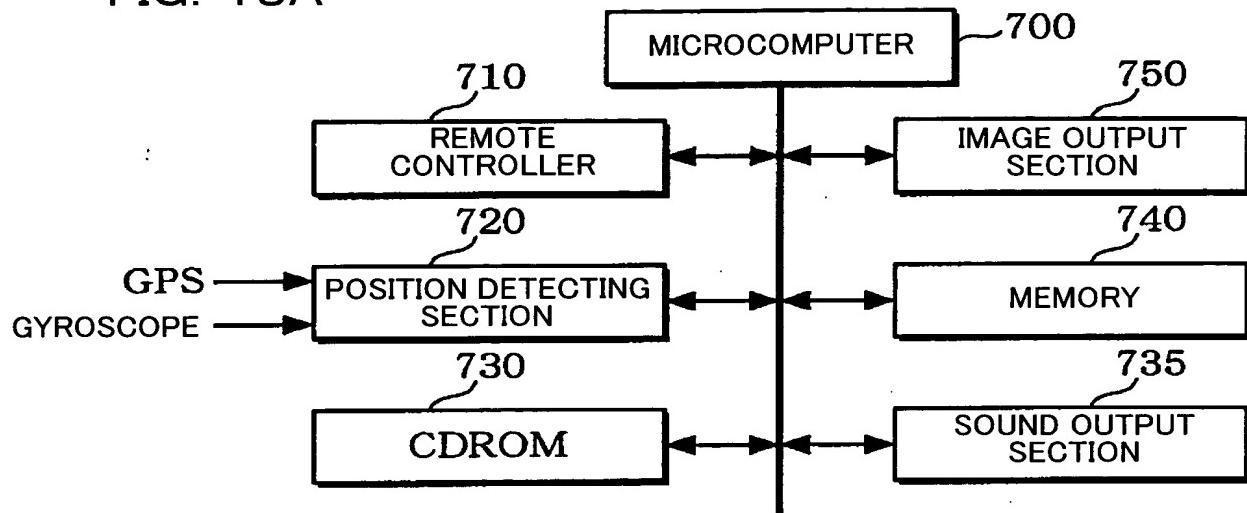


FIG. 18B

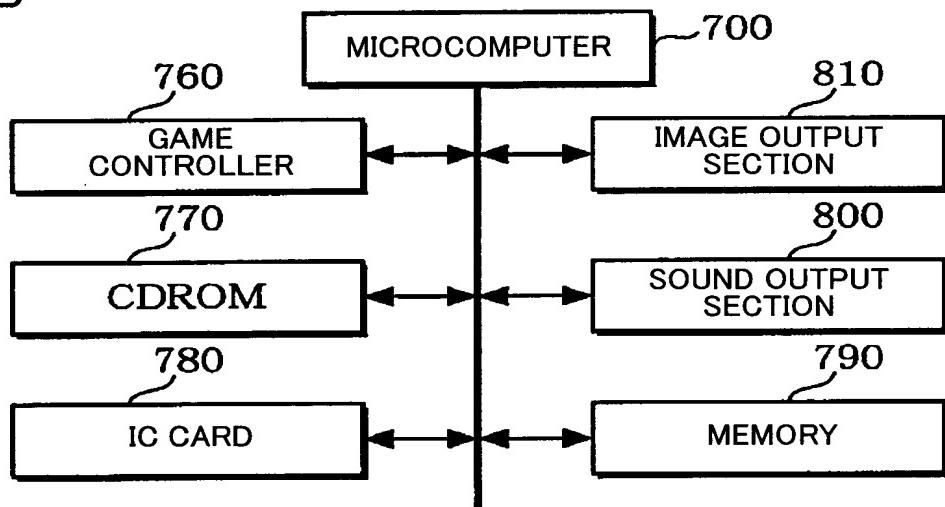


FIG. 18C

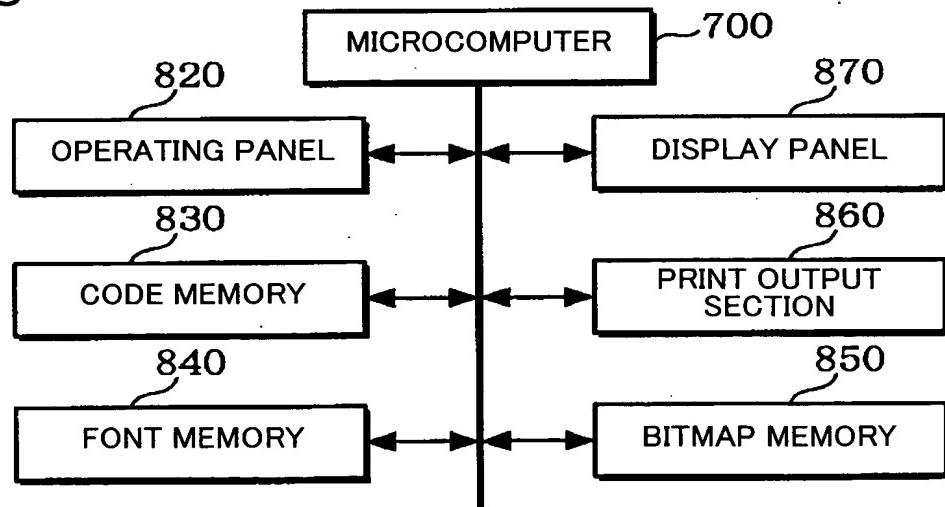


FIG. 19A

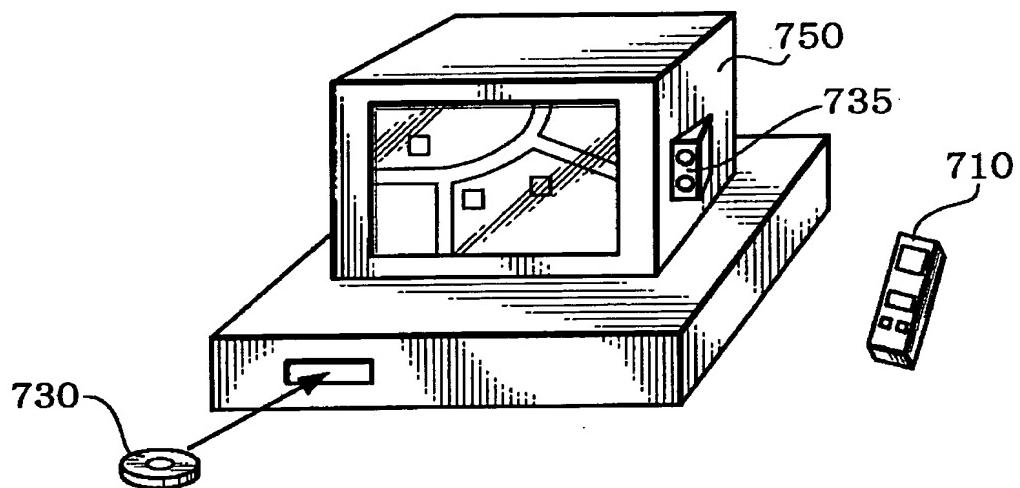


FIG. 19B

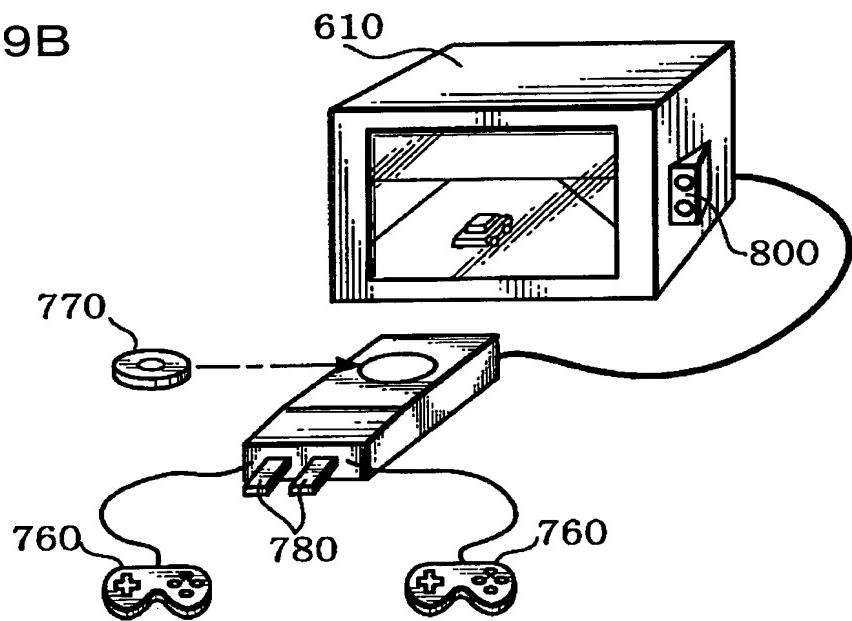


FIG. 19C

